



IFW

501.43128X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: B. Kuratomi, et al.  
Application No.: 10/682,028  
Filed: October 10, 2003  
For: FABRICATION METHOD OF SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE  
Group: 2891  
Examiner: Christian D. WILSON

**AMENDMENT**

September 20, 2005

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 20, 2005, please amend the above-identified application as listed in the following, and as set forth on the following pages:

Amendments to the Claims; and

Remarks are included following the amendments.